Xavier Institute of Engineering
Mahim, Mumbai 400016
Department of Computer Engineering
(Affiliated to University of Mumbai)

Academic Year 2022-23 (Even)

ACTIVITY REPORT on Hardware Security Integration with Pentesting

Organized By:	Dr. Vaishali Gaikwad
Date of the Event:	March 27, 2023
Venue:	Computer Center
Name of the Speaker(s):	Mr. Aditya Shende
Designation and Organization of the Speaker(s) :	Director, MI7, Pune
Participants Details:	SE
Number of Participants:	58

OBJECTIVE(s):

1. To understand underlying principles and many of the techniques associated with the Hardware Security Integration with Pentesting.
2. Recognize the importance of different processor architecture for analysis to achieve adequate perspectives of hardware security in various applications /devices with applying pentesting.

EVENT OUTCOME(s):

1. Students are able to understand underlying principles and many of the techniques associated with the Hardware Security Integration with Pentesting.
2. Students are able to recognize the importance of different processor architecture for analysis to achieve adequate perspectives of hardware security in various applications /devices with applying pentesting.

KEY POINTS:

Dr. Vaishali Gaikwad welcomed the participants, introduced the speaker to the audience and coordinated the guest lecture.

The expert speaker, Mr. Aditya Shende started the session by explaining the concept of Pentium processor and its use in the different embedded applications.

Further, following topics were covered

1. Architecture
2. Hardware security
3. Pentesting

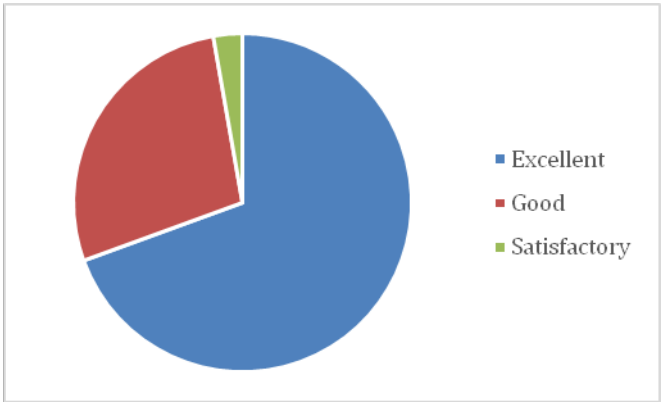
4. Real time applications

The lecture was followed by a Question-Answer session, facilitating the participants to get inputs from speakers on their queries related to the topic of the lecture. All speakers addressed the questions and provided their valuable recommendations for the same to the participants.

FEEDBACK:

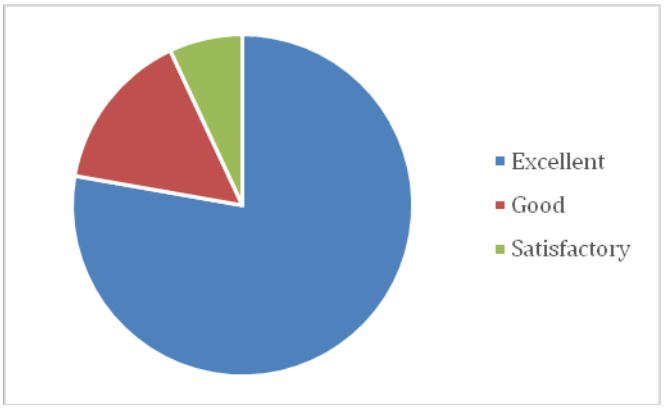
1. Rate the enhancement in your ability to apply engineering knowledge in developing and designing solutions for different problems through the hardware security in embedded systems.

58 responses



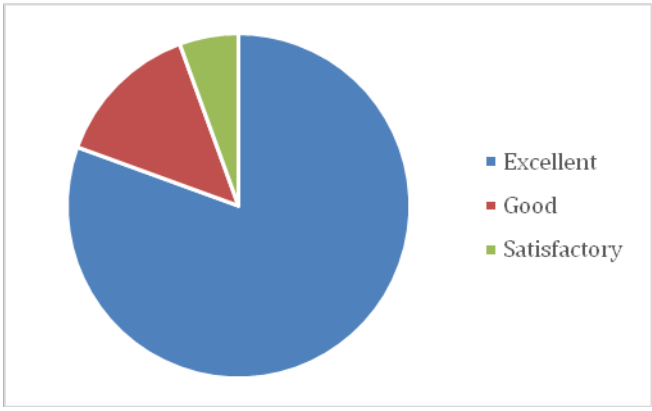
2. Rate your ability to understand underlying principles and many of the techniques associated with the Hardware Security Integration with Pentesting.

58 responses



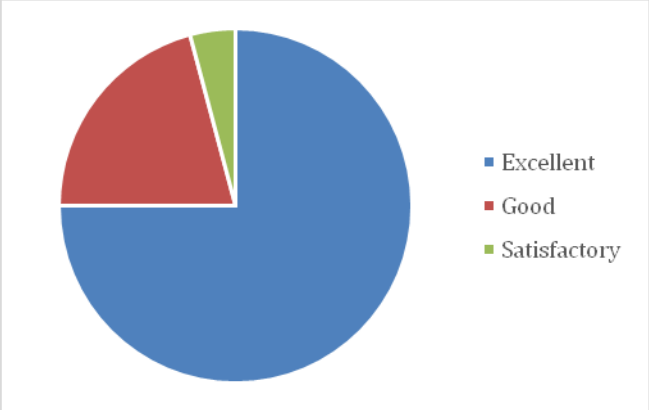
3. Rate the enhancement in your communication of and ability to apply modern tools and technology apply in real time application areas.

58 responses



4. Rate your ability to recognize the importance of different processor architecture for analysis to achieve adequate perspectives of hardware security in various applications /devices with applying pentesting.

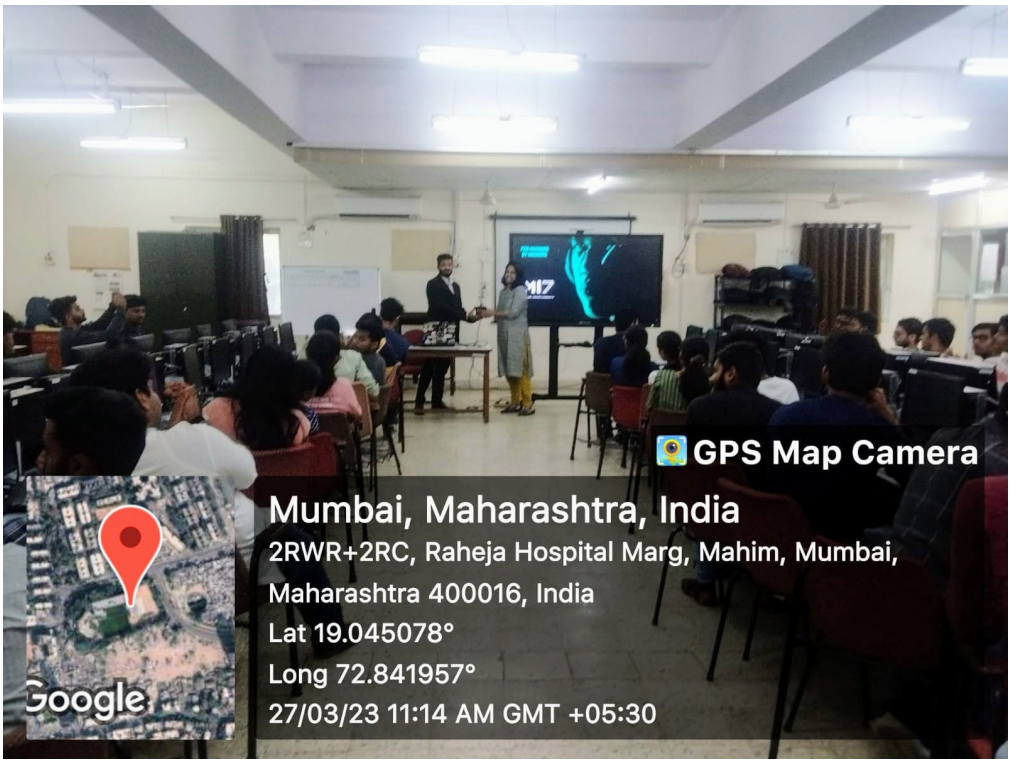
58 responses



PO MAPPING:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
EO1	3	2	2		2	3		3				2	2	
EO2	3	2	2		2	3		3				2	2	

GALLERY:



Felicitation of Speaker

Introduction of Speaker

Session conduction

Session conduction

Event Coordinator

Dr. Vaishali Gaikwad
Associate Professor,
Computer Engineering Department